

- L1: (2243) (711/141,147,148).CCLS.
- L2: (91835) cache
- L3: (120162) coherence\$4
- L4: (50816) multiprocessor or (multi\$4 adj processor)
- L5: (67935) directory
- L6: (2058) 2 and 3 and 4 and 5
- L7: (622) 2 same 3 same 4 same 5
- L8: (654272) local
- L9: (714361) remote
- L10: (1005) 2 and 3 and 4 and 5 and 8 and 9
- L11: (64) 2 same 3 same 4 same 5 same 8 same 9
- L12: (128) coherence adj controller
- L13: (112) 12 and 4
- L14: (80) 12 and 4 and 5
- L15: (76) 12 and 2 and 4 and 5
- L16: (132) (2 and 3 and 4 and 5).clm.
- L17: (93) 16 not (11 or 15)

- Failed
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- Queue
- Trash

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U	I	Document ID	Issue Date	Pages	Inventor	Title	Current OR	Cu
1	<input type="checkbox"/>	JP 2000298659 A	20001024	6	JOSEPH, D J et al.	Cache coherence management system for use in computer networks		
2	<input type="checkbox"/>	US 20010010068 A	20010726	13	MICHAEL, M M et al.	Maintenance system of consistent cached copies of memory locations		
3	<input type="checkbox"/>	US 20010013089 A1	20010809	17	WEBER, WOLF-DIETRICH	CACHE COHERENCE UNIT FOR INTERCONNECTING AII	711/146	711
4	<input type="checkbox"/>	US 6055610 A	20000425	11	Smith; Kenneth K. et al.	Distributed memory multiprocessor computer system	711/156	711
5	<input type="checkbox"/>	US 6295598 B1	20010925	14	Bertoni; Jonathan L. et al.	Split directory-based cache	712/28	712
6	<input type="checkbox"/>	US 6374331 B1	20020416	15	Janakiraman; Ganap Krishnan et al.	Distributed directory cache	711/141	711
7	<input type="checkbox"/>	US 6560681 B1	20030506	11	Wilson; James Christopher et al.	Split sparse directory for a distributed shared memory multinode computer system	711/144	711
8	<input type="checkbox"/>	US 6792512 B2	20040914	15	Nanda; Ashwini et al.	Method and system for organizing coherence directories	711/144	711
9	<input type="checkbox"/>	US 6901485 B2	20050531	32	Arimilli; Ravi Kumar et al.	Memory directory management in a multi-node computer system	711/135	711